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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*  
 This application is a DIV of 10/014,949 10/26/2001 PAT 6,737,722

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
 JAPAN P2001-128187 04/25/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature: <i>[Signature]</i> Initials: <i>[Initials]</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 1
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TITLE  
 METHOD OF FABRICATING SEMICONDUCTOR INTEGRATED CIRCUIT

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )
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